

# Discussion Points

- Key focus: using resilience to enable continued silicon scaling
- Challenge: how to do this within tight budgets
  - For soft and hard faults, at low to mid rates
  - Software assist is a good avenue for embedded
  - Observed that CMPs and SoCs will facilitate goals
- Challenge: how to let our solutions enable other resilience domains
  - Observed that we make the COTS parts that they use
- Challenge: where are the tools
  - Many great research problems to be solved here